## **REMARKS**

The specification has been amended to provide the required cross-referencing under 35 U.S.C. §120. The title has been amended to correspond to the title of the parent application.

Claims 8-12 and 17-20 have been canceled. Claim 1 has been amended to correct minor typographical errors, and to recite that the microprocessor is programmed to execute a field averaging algorithm to compensate for changes in quiescent Hall Effect voltages. Support for the amendment is found on page 6, lines 13-17. Claim 13 has been amended to incorporate the subject matter of claim 12, now canceled.

Entry and favorable consideration of the requested amendments is respectfully requested. No new matter has been added by any amendment herein.

Upon entry of this Preliminary Amendment, claims 1-7 and 13-16 remain pending. Applicants respectfully submit that the claims are in condition for allowance, which action is earnestly solicited.

The Assistant Commissioner is authorized to charge any fee which may be due in connection with this communication to Deposit Account No. 23-1703.

Dated: July 7, 2003

Respectfully submitted,

Andrew Fessak Reg. No. 48,528 Agent for Applicants

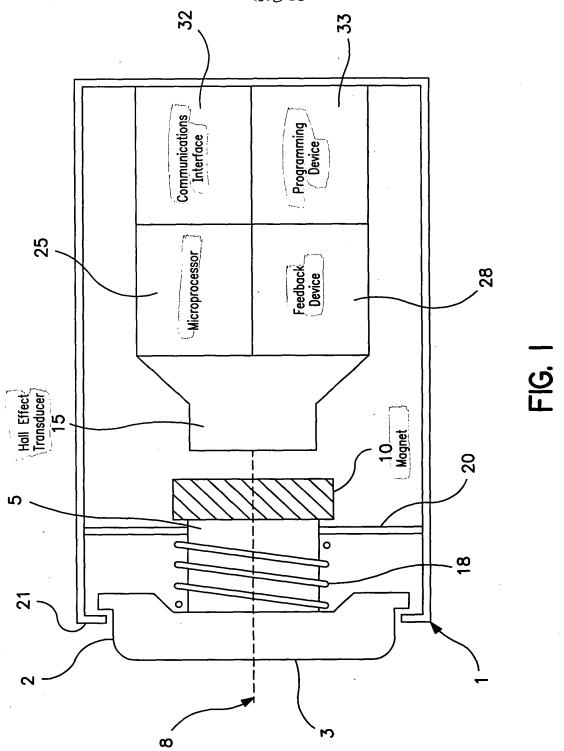
Customer No. 007470 White & Case LLP

Direct Dial: (212) 819-8437

Serial No.: TBA, filed TBA Docket No. 1116109-0009-CON

Inventors: Robert Sweet et al. Sheet 1 of 3

Markup version of substitute Drawings showing changes made



Serial No.: TBA, filed TBA Docket No. 1116109-0009-CON

Inventors: Robert Sweet et al. Sheet 2 of 3

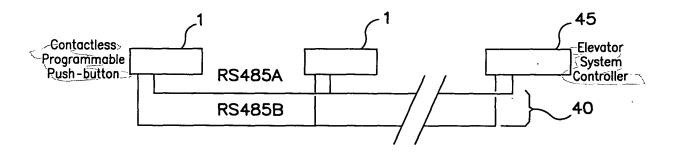


FIG. 2

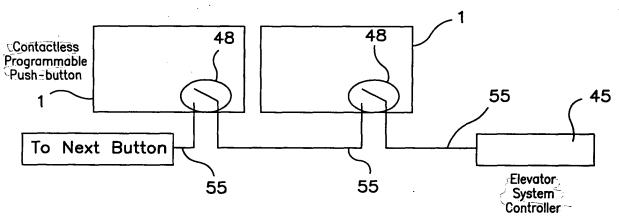


FIG. 3

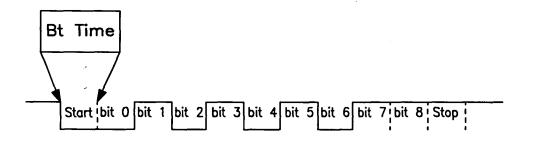


FIG. 4

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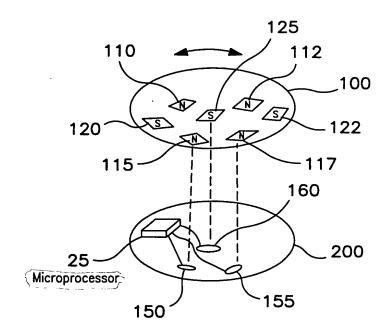


FIG. 5

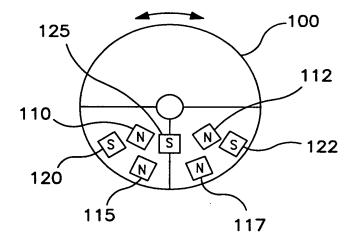


FIG. 6